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Appl. No. 10/829,320

Attorney Docket No. 81790.0312

Customer No.: 26021



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

Tomomi MOMOHARA

Serial No: 10/829,320

Filed: April 21, 2004

For: SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE AND METHOD OF  
TESTING THE SAME

Art Unit: Not Assigned

Examiner: Not Assigned

**PRELIMINARY AMENDMENT**

Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

I hereby certify that this correspondence  
is being deposited with the United States  
Postal Service with sufficient postage as  
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Mail Stop Amendment  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

July 26, 2004

Date of Deposit

Anthony J. O'Leary, Reg. No. 41,232

Name

July 26, 2004

Signature

Date

Dear Sir:

Prior to the first Office Action in the present application, please enter and  
consider the following amendments and remarks:

**Amendments** to the specification begin on page 2 of this paper.

**Amendments** to the Claims are reflected in the Listing of Claims which begins on  
page 10 of this paper.

**Amendments** to the Abstract begin on page 16 of this paper and include both an  
attached replacement sheet and an annotated sheet showing changes.

**Remarks/Arguments** begin on page 18 of this paper.